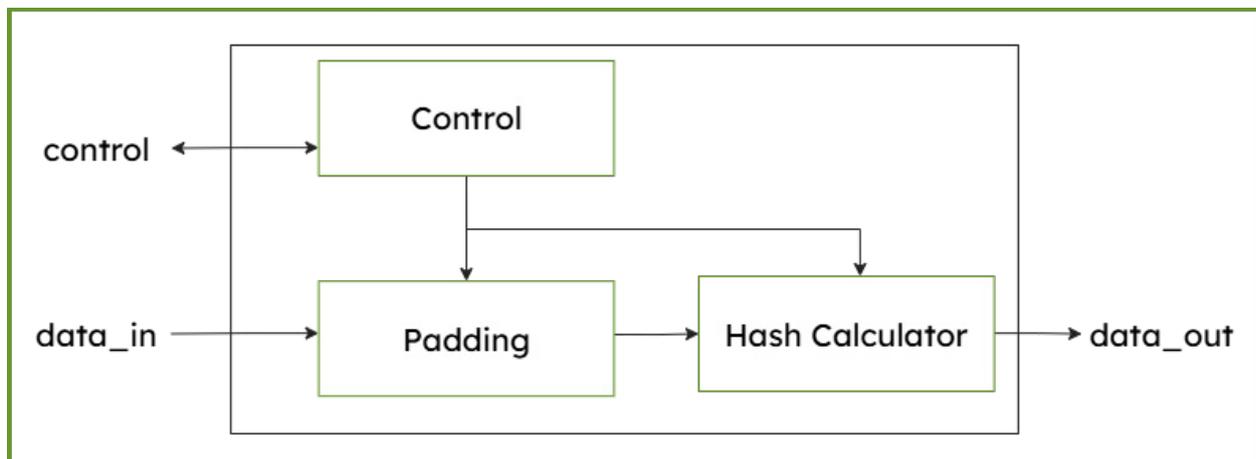


KiviHash-SHA3 Secure Hash Algorithm (224/256/384/512)

The KiviHash-SHA3 (secure hash algorithms) is a hardware accelerator for cryptographic hashing functions. It is an area efficient a high throughput design and compliant to NIST's FIPS 202 standard. It supports all SHA-3 hash functions (SHA-3-224, SHA-3-256, SHA-3-384 and SHA-3-512) as well as extendable output functions (XOF), SHAKE-128 and SHAKE-256. It provides full protection against time-based side channel attacks (SCA). Automatic byte padding is included. It operates in a single clock domain and has been extensively verified.



Key Features

- NIST FIPS 202 compliant
- Supports cryptographic hashing for SHA-3 in 224/256/384/512 mode
- Supports cryptographic hashing for Keccak in 224/256/384/512 mode
- Extendable-Output Functions for SHAKE 128/256
- AMBA® AXI4-Lite Interface
- Fully synchronous design
- HAL and software driver (C-code, platform independent)
- Platform agnostic design for any FPGA and ASIC

Applications

- Boot chains: Verifying firmware integrity from root of trust to application
- Firmware Updates: Detecting tampered or corrupted update packages
- FPGA bitstreams: Ensuring only trusted configurations are loaded
- Communication protocols: TLS, IPsec, MAC-based authentication
- External memory integrity: Protecting stored data in flash or external RAM

Test and Verification

- NIST CAVS test vectors for SHA3 hash functions
- NIST CAVS test vectors for SHA3 XOF functions
- Extended verification through simulation
- FPGA integration and implementation tests
- Unity tests for driver and whole IP Core

Licensing & Services

- Product license
 - One-time license fee
 - Single or multi project license
- Evaluation license available
- Technical support by email
- Maintenance & updates

Easy integration

- AMBA® AXI4 lite Interface
- Platform agnostic C source code HAL, API and software driver
- Software examples included
- Software user guide

Deliverables

- Product license
 - System Verilog RTL Source Code or Netlist format
- Free evaluation license
 - Netlist format, time bombed
- Testbenches
- Integration examples
- Software HAL & driver source code
- Software example
- Documentation

FPGA Implementation Results

AMD	Logic (LUTs)	fmax (MHz)	Max. Throughput (Gbps)
Spartan 7	7687	171.5	8.4
Kintex 7	7722	258.5	12.4
Zync MPSoC Us+	7730	340.5	16.3
Versal AI Cores Series	7572	427.4	20.5

Efinix	Logic (XLR)	fmax (MHz)	Max. Throughput (Gbps)
Titanium	9246	375.7	18.0
Topaz	9246	234.6	11.3
Trion	9246	102.9	4.9